



[10191/2251]

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Inventor(s) : SPITZ et al. Confirmation No.: 7205
Serial No. : 10/070,521
Filed : July 8, 2002
For : ARRANGEMENT HAVING P-DOPED AND N-DOPED SEMICONDUCTOR LAYERS, AND METHOD FOR THE MANUFACTURE THEREOF
Examiner : J. Jackson
Art Unit : 2815
Customer No. : 26646
I hereby certify that this correspondence is being deposited with the United States Postal Service with sufficient postage as first class mail in an envelope addressed to:
Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450
on
Date 3/22/06 Atty's Reg. #41,172
Atty's Signature L02
DERVIS MAGISTRE
KENYON & KENYON LLP

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Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

RESPONSE TRANSMITTAL

SIR:

Please find a Response transmitted herewith for filing in the above-identified patent application.

No fee is believed to be required. However, if any fee is required, please use Deposit Account No. 11-0600. A duplicate copy of this transmittal letter is enclosed for that purpose.

Respectfully submitted,

KENYON & KENYON LLP

Dated: 3/22/06

By: L02 (B. No. 41,172)

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DERVIS MAGISTRE
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RESPONSE

SIR:

In response to the Office Action dated February 13, 2006, reconsideration and allowance of the above-referenced application are requested for the reasons below.

Amendments to the Specification do not appear in this paper.

Amendments to the Claims do not appear in this paper.

Amendments to the Drawings do not appear in this paper.

Remarks begin on page 2 of this paper.